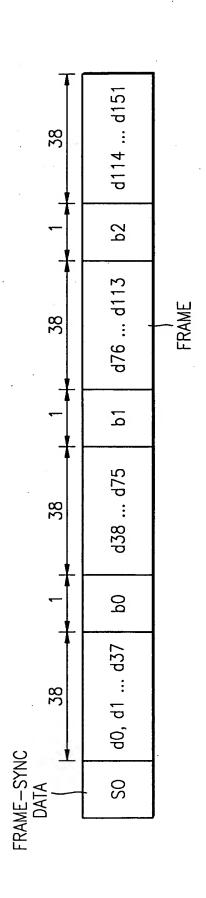
FIG. 1 PRIOR ART

	16 PHYSICAL SECTORS
	38 ECC COLUMNS
DATA BLOCK	ONE BIS COLUMN
· .	38 ECC COLUMNS
	ONE BIS COLUMN
	38 ECC COLUMNS
	ONE BIS COLUMN
	38 ECC COLUMNS
	FRAME—SYNC DATA
	496 FRAMES

FIG. 2 PRIOR ART

TITLE: APPARATUS AND METHOD GENERATING ERROR FLAG ... INVENTORS: Sung-hee HWANG et al. APPLICATION NO.: New DOCKET NO.: 1793.1155



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FIG. 5

FRAME-SYNC ERROR MEMORY	
FIRST FRAME-SYNC ERROR MEMORY (1x496 bits)	
SECOND FRAME-SYNC ERROR MEMORY (1x496 bits)	
•	
N-th FRAME-SYNC ERROR MEMORY (1x496 bits)	

FIG. 6

BIS ERROR FLAG MEMORY	
FIRST BIS ERROR FLAG MEMORY (1x496 bits)	
SECOND BIS ERROR FLAG MEMORY (1x496 bits)	
•	
M-th ERROR FLAG MEMORY (1x496 bits)	

TITLE: APPARATUS AND METHOD
GENERATING ERROR FLAG ...
INVENTORS: Sung-hee HWANG et al.

APPLICATION NO.: New DOCKET NO.: 1793.1155

FIG. 7

